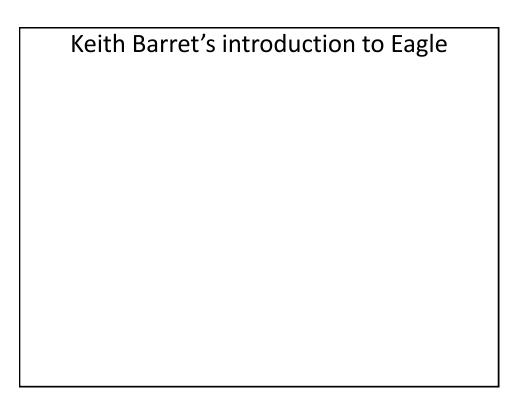
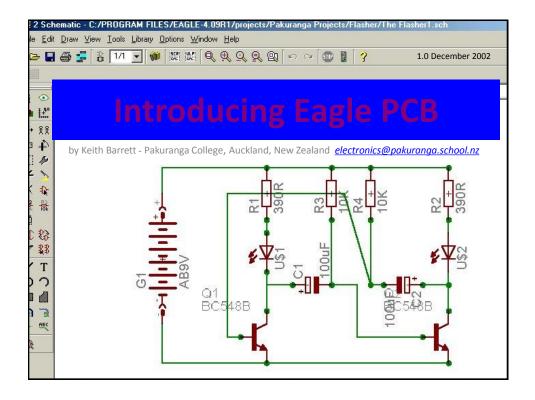
ETH Course 402-0248-00L: Electronics for Physicists II (Digital)

- 1: Setup uC tools, introduction
- 2: Solder SMD AVR32 board
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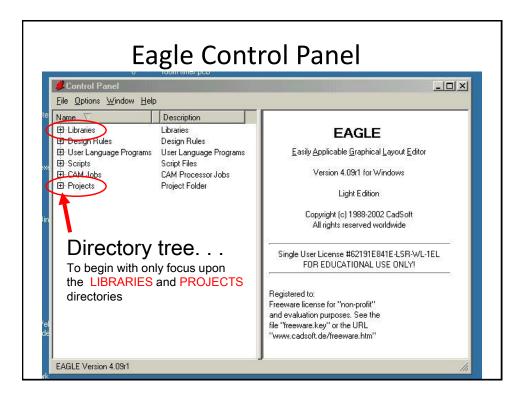
Pr	inted Circuit Board	(PCB) design to	ols
	Pros	Cons	Cost
Eagle (cadsoft)	Free (simple boards) Easy to learn Truly cross platform	Clunky interface Limited router	Free for limited version. 345 EUR for full version if student.
Altium	Powerful	Windows only	3k CHF Or 300/yr ETH
Cadence	Really powerful	Arcane (l)unix only	\$\$\$ except ETH has license seats

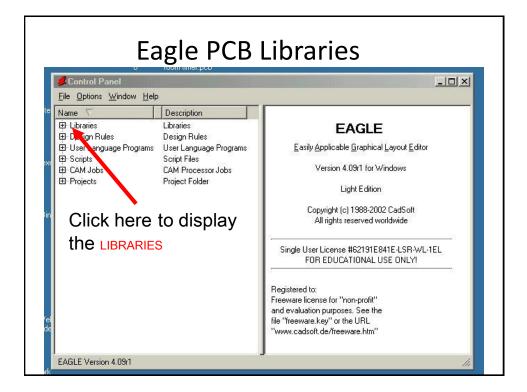


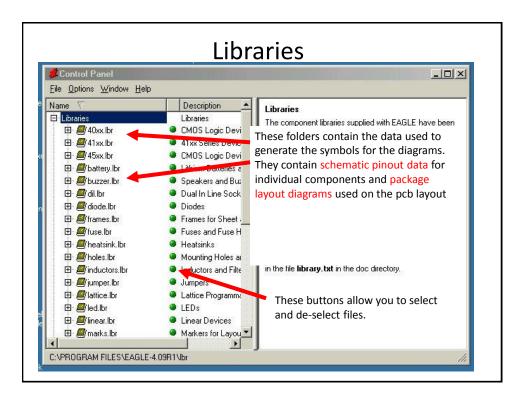


	Eagle costs	
EAGLE Light	(1 schematic sheet, 2 signal layers, 100x80mm routing area)	Free for Educational purpose
EAGLE Standard	(99 schematic sheets, 6 signal layers, 160x100mm routing area)	http://www.cadsoftusa.co m/shop/pricing/
EAGLE Hobbyist	(99 schematic sheets, 6 signal layers, 160x100mm routing area; for individual, non commercial use only)	http://www.cadsoftusa.co m/shop/pricing/
EAGLE Professional	(999 schematic sheets, 16 signal layers, 4m x 4m routing area)	http://www.cadsoftusa.co m/shop/pricing/

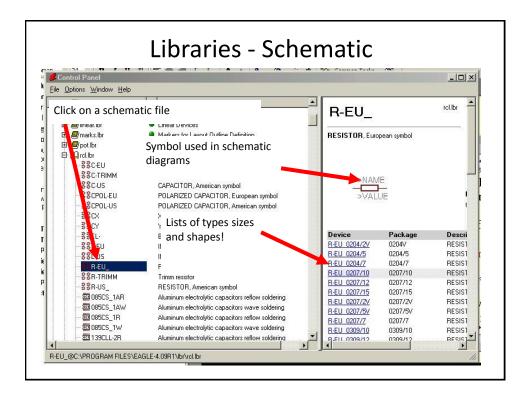
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 ➡ Libraries ➡ Design Rules ➡ User Language Programs ➡ CAM Jobs ➡ Projects This is the control which should apper programme opens 	ear when the	EAGLE Easily Applicable Graphical Layout Editor Version 4.09r1 for Windows Light Edition Copyright (c) 1988-2002 CadSoft All rights reserved worldwide Single User License #62191E841E-LSR-WL-1EL FOR EDUCATIONAL USE ONLY1
		Registered to: Freeware license for "non-profit" and evaluation purposes. See the file "freeware.key" or the URL "www.cashoft de/freeware.htm"

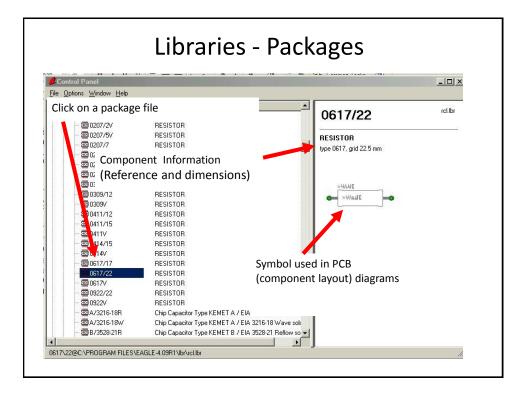


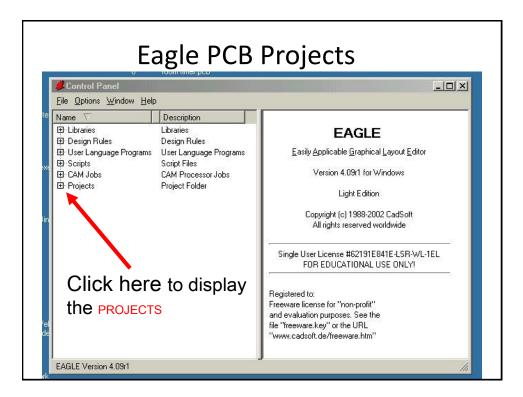


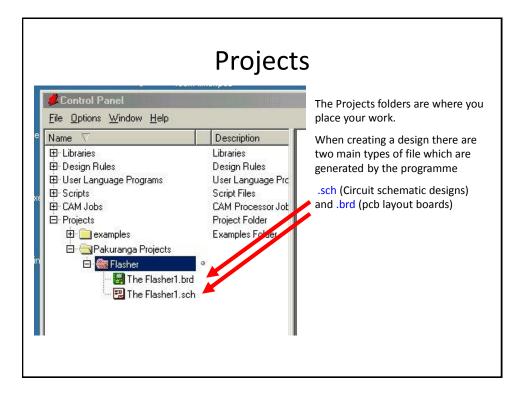


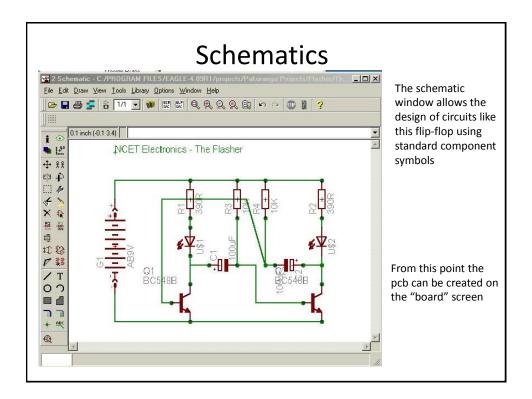
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	CAPACITOR, American symbol POLARIZED CAPACITOR, European POLARIZED CAPACITOR, American CAPACITOR X CAPACITOR Y CAPACITOR Y CAPACITOR
	BIPOLAR ELECTROLYTIC CAPACI [*] File content descriptions INDUCTOR, Eugenen symbol INDUCTOR Menerican symbol RESISTOR, European symbol RESISTOR, American symbol
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🖾 139CLL-2R 🖾 139CLL-2W 🖾 139CLL-2W	Aluminum electrolytic capacitors reflow soldering Aluminum electrolytic capacitors wave soldering Aluminum electrolytic capacitors reflow soldering

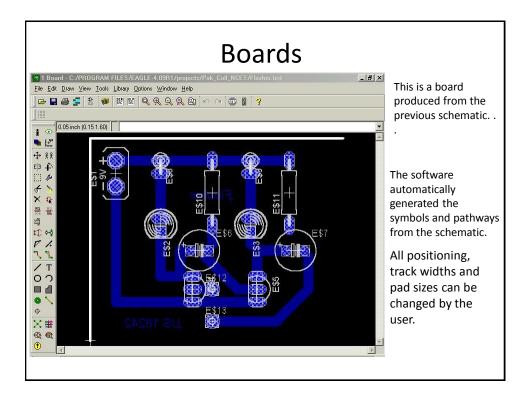


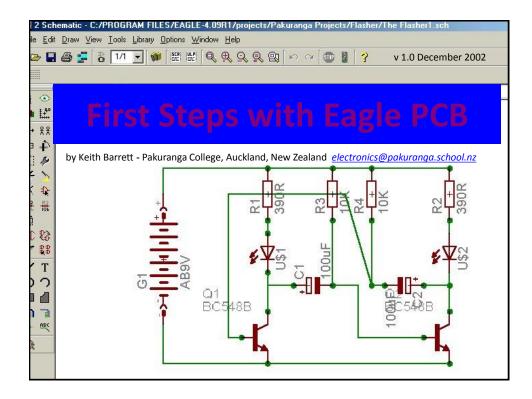


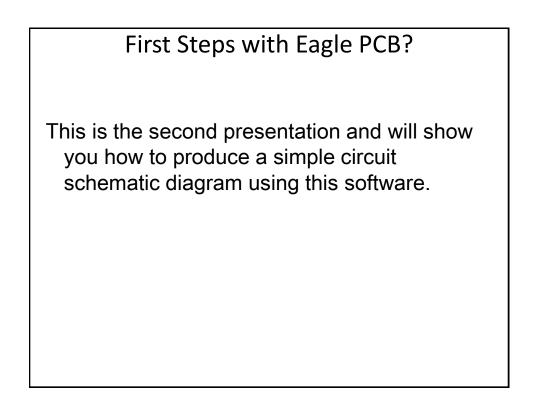


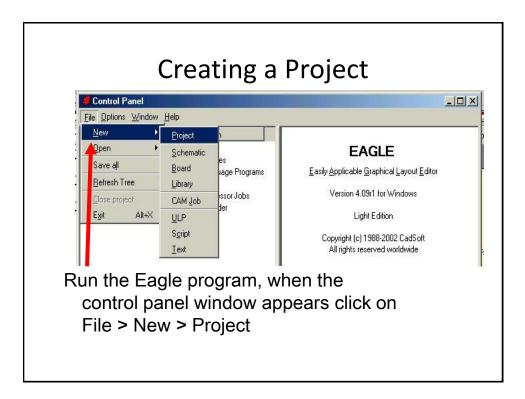






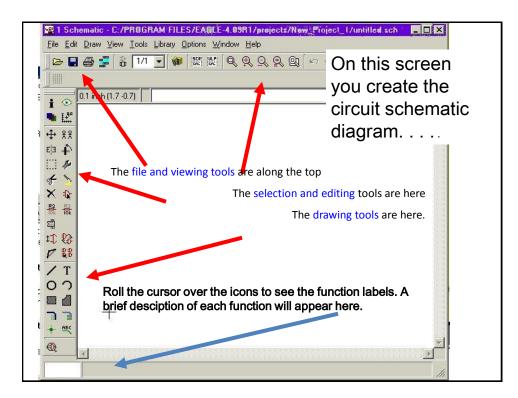


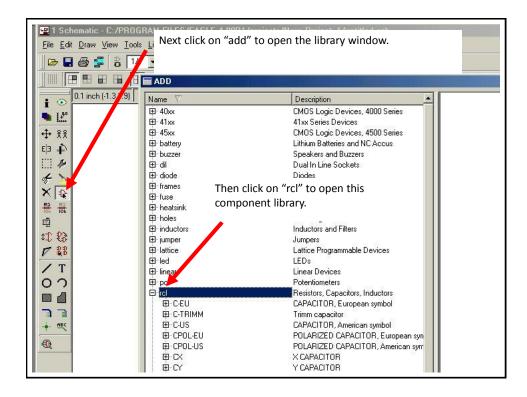


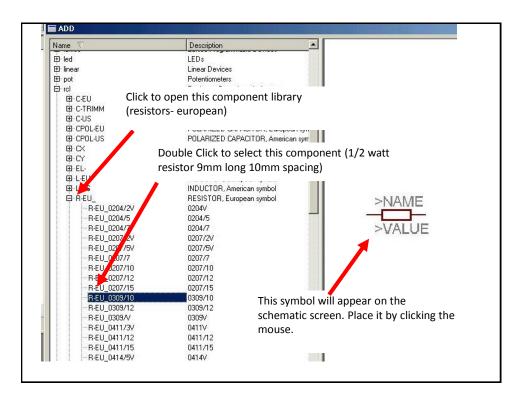


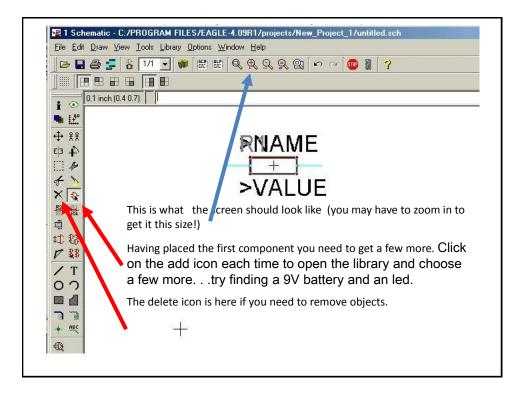
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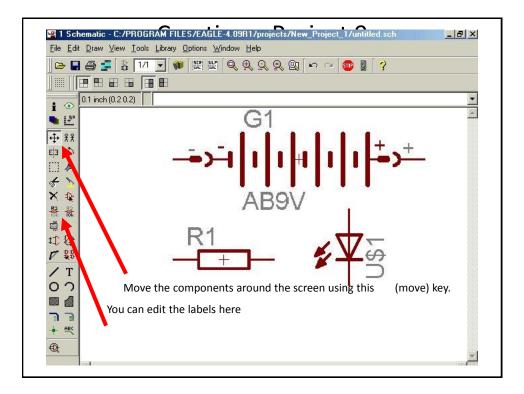
ile <u>Options</u> <u>W</u> indow	Help
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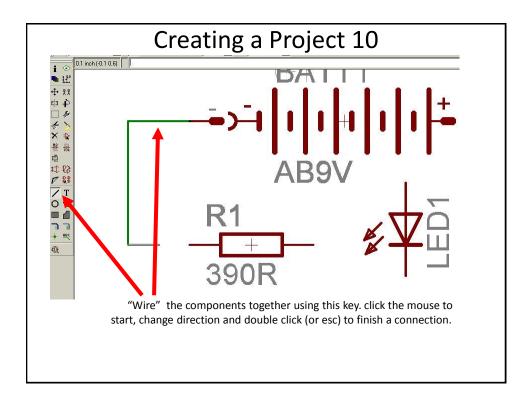


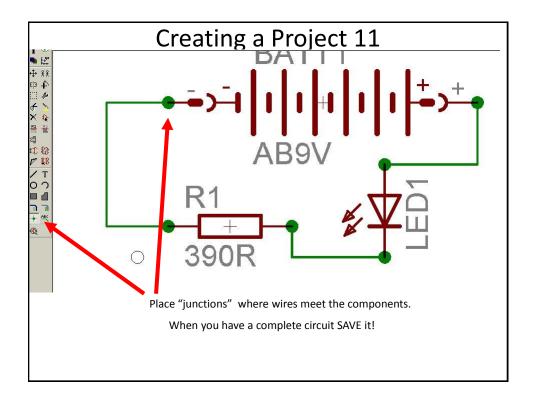


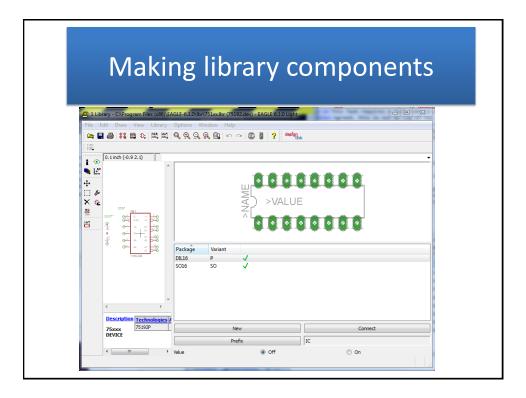


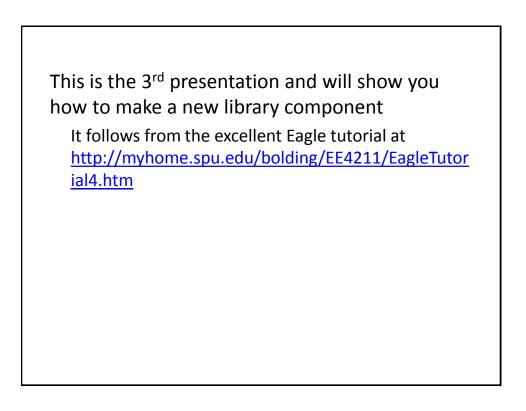


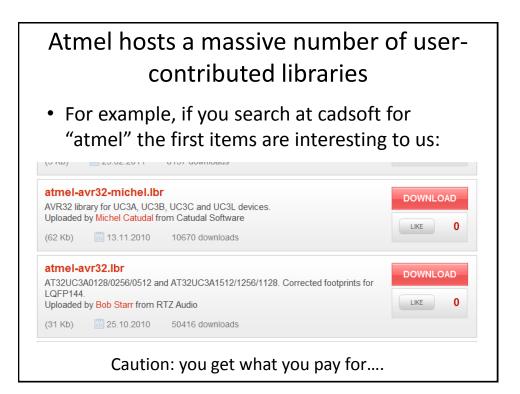


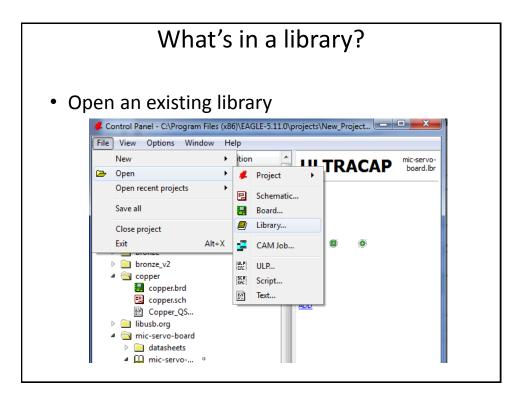


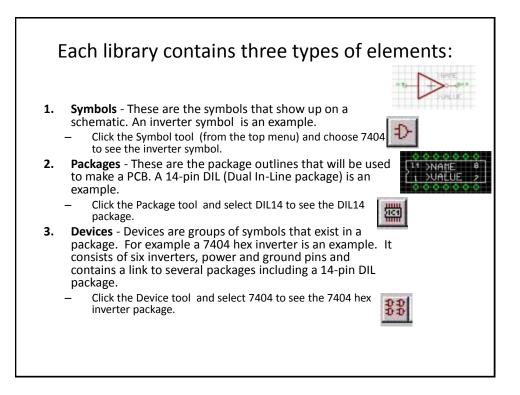


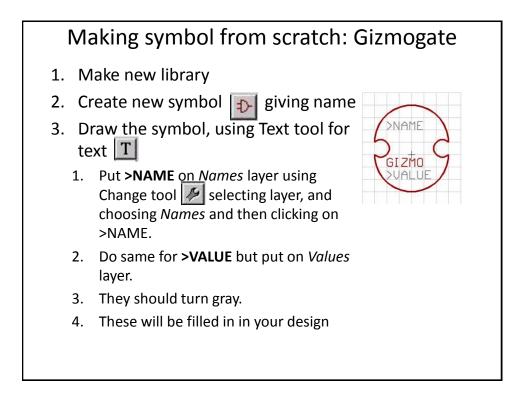


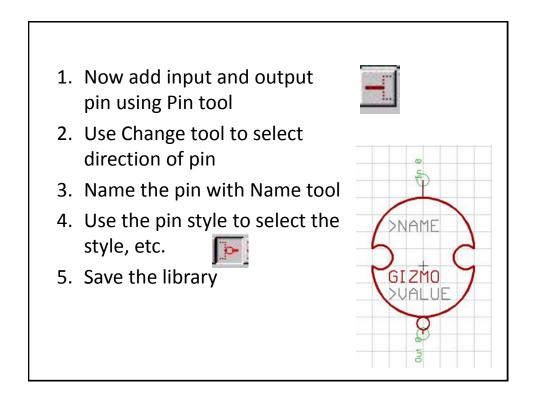


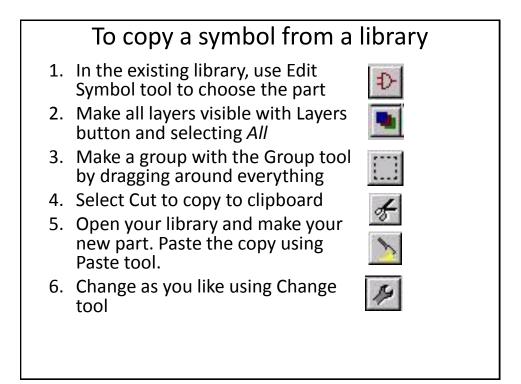




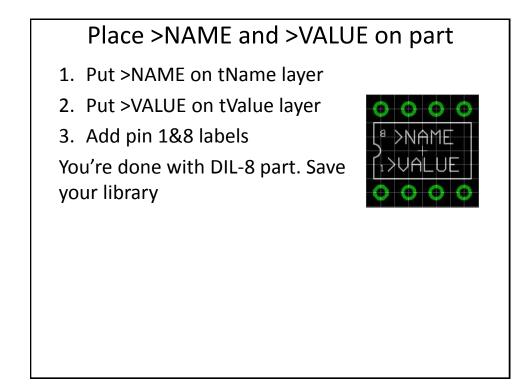






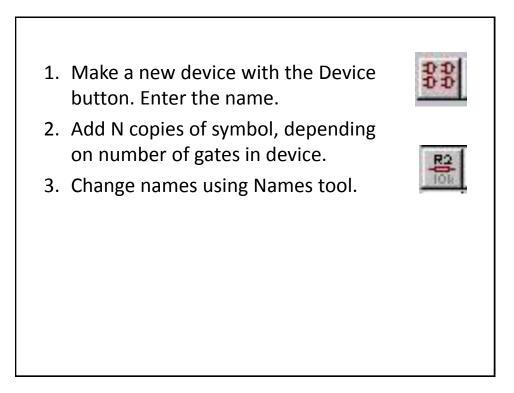


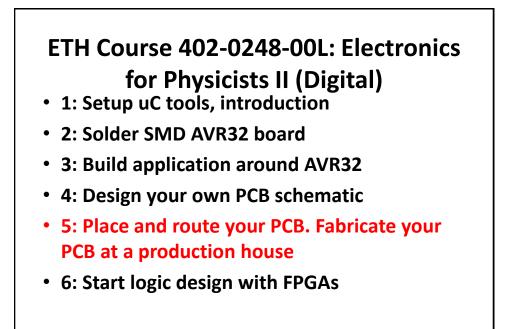
Making a package from scratch 1. Select Package, enter name DIL-8 2. Select Grid tool and select grid 0.025 with multiple of 2 3. For through hole parts, you need holes in right place and large enough! (Common error, holes too small!) Use the Hole tool, place pads CCW from bottom left. 4. For SMD, use the SMD Pad tool 5. Draw the outline on layer *tPlace*.

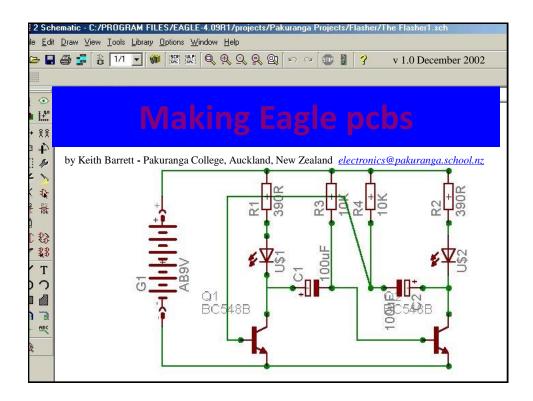


Putting symbols and packages together

The final task in making a part is to create a *device* that has information on how symbols are placed inside of a package. For many devices, there will be only one symbol. However, many devices contain multiple symbols, as well as hidden power pins.

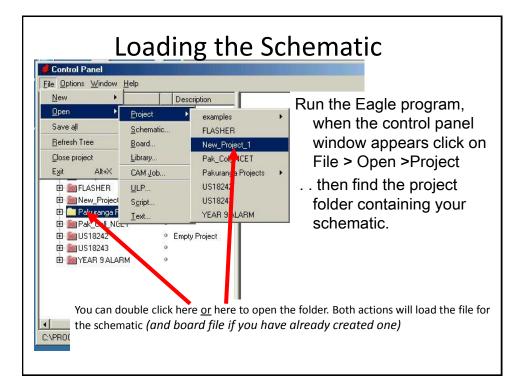




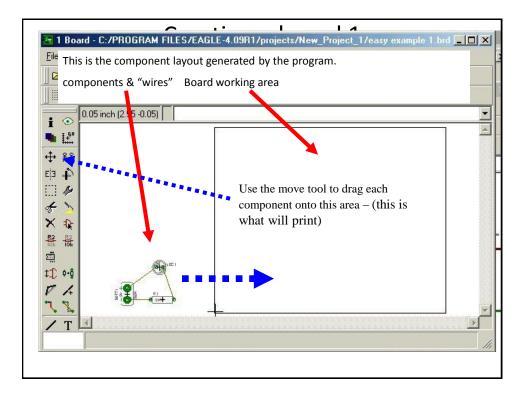


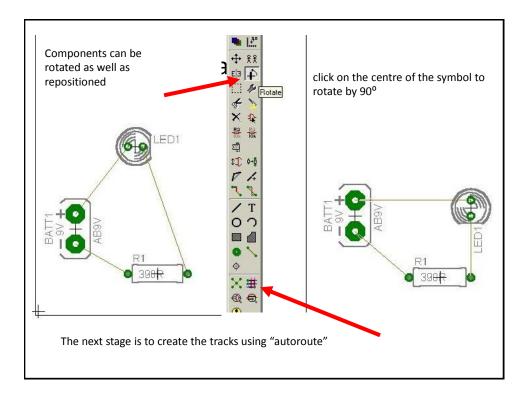
Making Eagle PCBs?

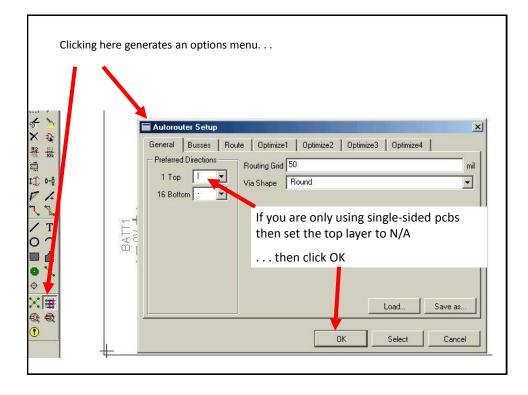
This is the 4th presentation and will show you how to produce a simple, single layer printed circuit board from a circuit schematic diagram using this software.

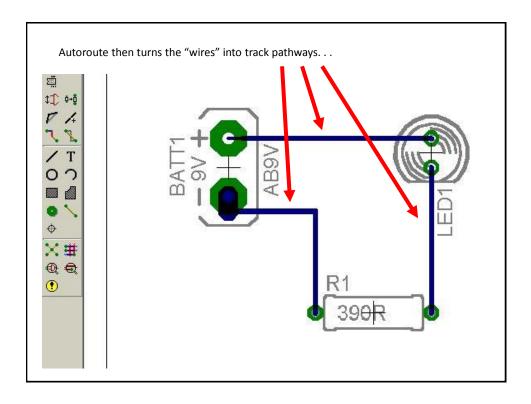


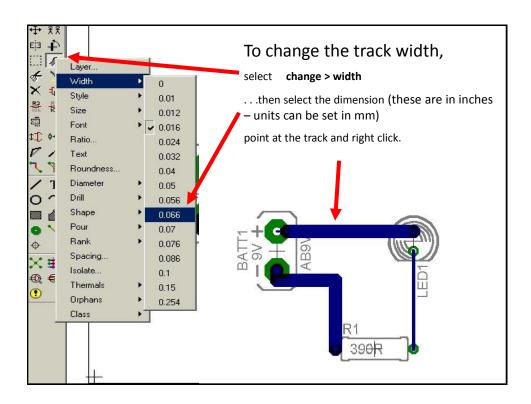
New Di Open Save 1.1	When the schematic has loaded select
Save <u>a</u> s Saveaļi Printsetup	File > Switch to board
<u>Print</u> CA <u>M</u> Processor	مند <u>منارا المارا التحميم</u>
Switch to board Export Script <u>B</u> un Close	The software will then check to see if a board file exists to go with schematic. If it does it will load the file, otherwise it will offer to create one automatically.
T , Warning	
	e from schematic?

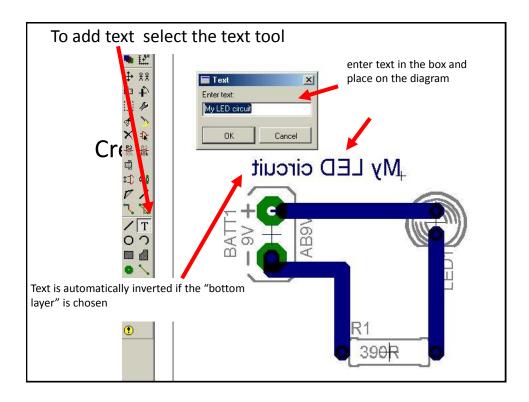






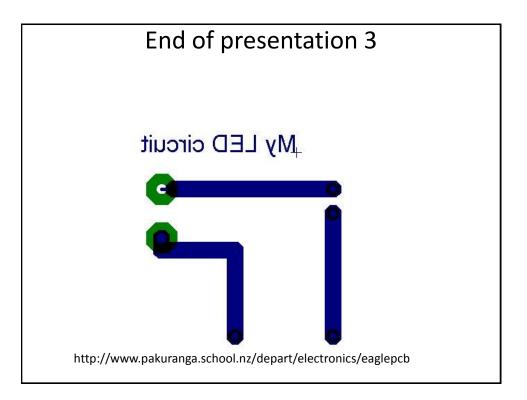






Choose the	layers to print sel	ect View > Display/hide
🚾 1 Board - C:/	PROGRAM FILESZEAG	LE-4.09R1/proje
<u>File Edit Draw</u>	View Lools Library Opti	ons <u>W</u> indow <u>H</u> elj
🛛 🗁 🖬 🎒 🛓	<u>G</u> rid	
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*** **	<u>B</u> edraw F2	
Eja 🎝	Zoom to <u>f</u> it Alt+	F2
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5 >	Zoom <u>o</u> ut F4	
× 😰	Zoom select	
	1	

Display X	4
Layers:	Here you can select
Nr Name	
1 Top	what appears on
16 Bottom	inde appears on
17 Pads	screen and the
18 Vias	
19 Unrouted	printer. (blue = selected /
20 Dimension	1 • · ·
21 tPlace	white = deselected)
22 bPlace	
23 tOrigins	
24 bOrigins	
25 tNames	Click on the numbers to turn
26 bNames	on/off. Click on the colour to edit
27 Values	the colour scheme
28 bValues	the colour scheme
29 tStop	For most single sided pake
30 bStop	For most single sided pcbs
31 Cream	everything should be off except
32 bCream	layers 16,17 (tracks & pads) 45
33 tFinish	
34 bFinish	drill holes)
35 tGlue	
36 bGlue	
New Change Del	
	Save and print your board.
All None	
0K Cancel	



ETH Course 402-0248-00L: Electronics for Physicists II (Digital)

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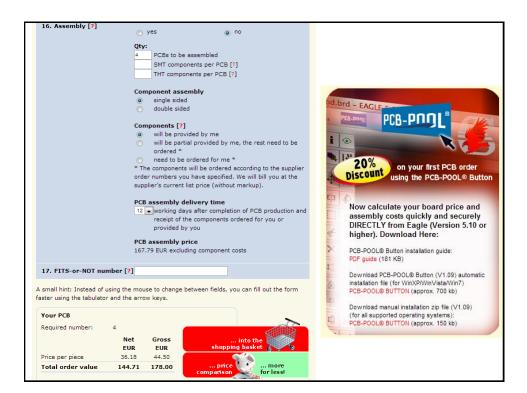
How do you get your PCB fabricated?

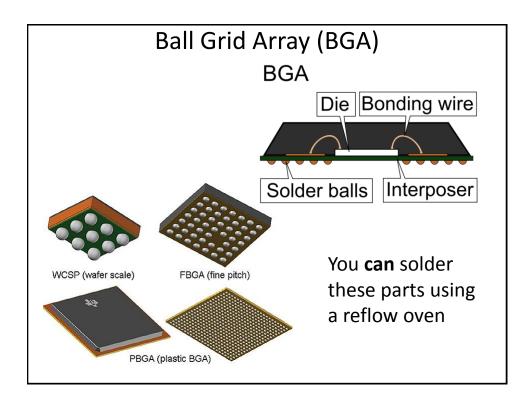
- 1. Have your own PCB router/driller machine.
 - Starts with blank PCB covered with metal, mills away all metal and places plugs for vias.
- 2. Or, send your board design away for fabrication

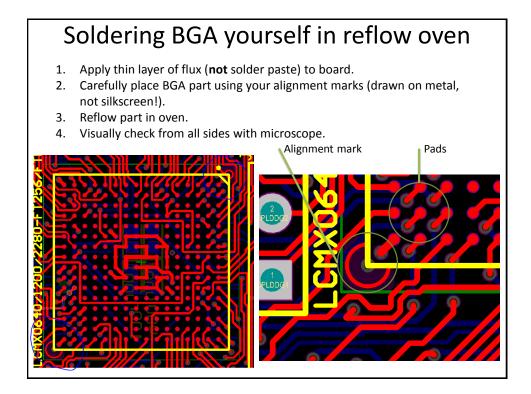
Example of having board fabricated You need to know • 1. Board dimensions in mm x mm 2. Number of layers 3. Basic rules such as min track width and spacing, minimum hole size ← → C f www.beta-layout.com 🌊 dig 🛐 Cal 🜊 🔨 viki 🛐 Google 👍 docs 🎐 music 🍇 trans 🏦 edoz 🎦 contacts 🥥 HTV 🛛 L& ebank 🗋 PO 👳 news 🛐 acct 💹 g+ 🚺 /. 😡 sch 🥥 TI 😤 This page is in German - Would you like to translate it? Translate Nope Always translate German LAYOUT create:electronics ESTORE: LASER-STENCIL' PCB-POOL' PANEL-POOL PCB-OVERSEAS PROTOTYPES PROTOTVPES in 🔛

http://www.pcb-pool.	com/ppuk/order_productconfiguration_js.htm
You are here: Order PCBs	
PCB CONFIGU	RATION
Other specifications?	Change to the HTML version
1. Layer count [?]	© 1 © 2 ⊙ 4 ⊙ 6
2. Quantity	1
3. Dimensions [?]	Length in mm: Width in mm:
4. Base material [?]	Please choose Layer count first!
5. Soldermask [?]	Please choose Layer count first!
6. Silkscreen [?]	Please choose Layer count first!
7. Surface [?]	Please choose Layer count first!
8. Layout specifications [?]	Please choose Layer count first!
9. Overdelivery [?]	Please choose Layer count first!
10. Magic PCB [?]	Please choose Layer count first!
11. E Test [?]	Please choose Layer count first!
12. Delivery time in WD	Please choose Layer count first!
13. File format: [?]	Please choose 💌
14. Project name:	

PCB CONFIGURATION			
Other specifications?	Change	to the HTML version	
1. Layer count [?]	○ 1	⊙ 6	
2. Quantity	2		
3. Dimensions [?]	Length in mm: 100 Width in mm:	100	
4. Base material [?]	 FR4, 35µmCu, 1,6mm FR4, 35µmCu, 1,0mm 		
5. Soldermask [?]	⊚ yes no		
6. Silkscreen [?]	 o yes, only top o yes, top and bottom o no 	, only bottom	
7. Surface [?]	● ENIG (Electroless Nickel Gold) for → HAL (Hot Air Leveling - lead-free a		
8. Layout specifications [?]	Min. track / gap size: > = 0.125mm (5mil) > = 0.150mm (6mil) Min. drill-end diameter: > = 0.2mm (8mil) > = 0.3mm (12mil)		
9. Overdelivery [?]	 ○ yes, if available ◎ no ○ yes, if available as Magic-PCB® - RFID chip and win a Reader/Writer 		







Reflow oven

- Applies temperature profile to slowly heat parts to avoid stress and allow volatile gases to escape, activates flux, then reflows solder
- Hint: Use gold finish on pads if you ever want to rework boards because gold does not oxidize

